

Skipper assembled PCB design documentation requirements

Date: 2019-02-22

Rev: 1

Introduction

This document describes the PCB design documentation requirements in order to ease the overhead of sharing developing, servicing and selling of HW products among Skippers collaborators.

Design files shall be documented using English written language.

Title field or file headings

All readable files shall, if possible, contain title field or text heading with relevant information like title, file name, board name, date of change, assemble revision, bare board revision and/or VCS file revision, if applicable for each case and each file.

Example of title field from the PCB outline Gerber file:



Example of BOM title field:

Designator	Description 1	Category 1	Comment	Footprint	Manufacturer 1
IC35	IC SCHMITT TRIG INV SOT-23-5	Integrated Circuits (ICs)	SN74LVC1G14DBVR	SOT-23-5	Texas Instruments
IC40	IC GATE AND INH 2IMP 5-TSOP	Integrated Circuits (ICs)	SN74LVC1G14DBVR	SOT-23-5	TI/ST/DAI

The table also includes a 'Title' field with the following information:

- Title: Multi Extension Card
- Source Data From: Multi Extension Card.PRJPCB
- Project/Board: JB70 Multi Extension Card
- Part Number: ZZZ-07002 Multi Extension Card
- Revision: D3
- Report Date: 08.05.2018 16:33:43
- Print Date: 04.02.2019 17:58:24

File names

In general file names shall not be changed between different versions because VCS (Version Control System) are using the name for to identify files. That means that file names cannot contain variable revision / version or dates. For the same reason it is important to use the correct name first time. To differentiate files for different assemble options, the variant may be added as a part of the file name.

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 Multi Extension Card Assembled Schematic Prints(ZZZ-07002).PDF	08.05.2018 15:52	Adobe Acrobat D...	5 456 kB
 Multi Extension Card BOM ASM(ZZZ-07002).xlsx	13.01.2019 23:23	Microsoft Excel-re...	234 kB
 Multi Extension Card BOM COMP(ZZZ-07002).xlsx	13.01.2019 23:09	Microsoft Excel-re...	213 kB
 Multi Extension Card Export STEP(ZZZ-07002).step	08.05.2018 16:40	IDA-STEP file	22 956 kB
 Multi Extension Card Generates pick and place files(ZZZ-07002).txt	07.03.2017 09:58	TXT-fil	62 kB
 Multi Extension Card PCB 3D Print bottom(ZZZ-07002).PDF	08.05.2018 15:55	Adobe Acrobat D...	1 311 kB
 Multi Extension Card PCB 3D Print top(ZZZ-07002).PDF	08.05.2018 15:54	Adobe Acrobat D...	1 368 kB
 Multi Extension Card PDF3D(ZZZ-07002).PDF	08.05.2018 16:00	Adobe Acrobat D...	16 111 kB
 Multi Extension Card Assembly Drawings(No Variations).PDF	08.05.2018 16:02	Adobe Acrobat D...	1 785 kB
 Multi Extension Card Generates pick and place files All(No Variations).txt	08.05.2018 16:23	TXT-fil	133 kB
 Multi Extension Card PCB Special Mounting Instructions(No Variations).PDF	08.05.2018 15:57	Adobe Acrobat D...	1 503 kB
 Multi Extension Card PDF3D All(No Variations).PDF	08.05.2018 15:59	Adobe Acrobat D...	17 383 kB
 Multi Extension Card Schematic Prints(No Variations).PDF	08.05.2018 15:52	Adobe Acrobat D...	5 438 kB

Packing Design Files

Design files shall be packed into two separate ZIP-files, one for bare board production and one for assembling of the boards. The ZIP-files shall contain only generated production files, that means not any CAD source files. The ZIP-file name shall set together of the P/N followed by its revision.

ZIP-fil with bare PCB production files (Gerber files including PCB outline, + fabrication drawing/PCB Specification and Drill drawing)

 PCB130626002-4.zip	17.08.2016 12:51	Komprimert (zipp...	2 444 kB
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ZIP-fil with assembly PCB files (Schematic, BOM, Component placement, Pick & Place, Special mounting instruction..)

 ZZZ-07002-D3.zip	08.05.2018 16:50	Komprimert (zipp...	38 962 kB
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If there are several variants of the bare board PCB or the assembled PCB each variant is given its own P/N.

 PCB160106001-4.zip	07.09.2017 09:43	Komprimert (zipp...	1 775 kB
 ZZZ-07012-E1.zip	28.08.2017 13:42	Komprimert (zipp...	25 994 kB
 ZZZ-07027-E1.zip	28.08.2017 13:42	Komprimert (zipp...	26 068 kB

The CAD source files are the source of the design. However, that is the production files which are the source of the production units. The production files are in turn a result of the CAD source files and the current available component data collected at the time of generation. It is important to store and keep control of all generated files for all production revisions. It is necessarily not possible to regenerate the same production files later.

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CAD source files must be stored separated from generated production files. Source files are intended for development purposes only. The source files must be tagged and stored so that it is possible to retrieve source files for all produced versions of the PCBs.

If the source files are to be sent physically or by email the files can be retrieved and added to a separate ZIP file. Then the ZIP file shall be named with the board name followed by its revision number.

Typical source file types for Altium files (SchDoc, PcbDoc, PrjPCB, SchLib, PcbLib, BomDoc, OutJob)

 Multi Extension Card-D3.zip 04.02.2019 17:23 Komprimert (zipp... 38 423 kB

Package	File Name	Path in Zip File
	Skipper.PcbLib	A Share LIB\Skipper.PcbLib
	Multi Extension Card.PRJPCBStructure	A\Multi Extension Card\Multi Extension Card.PRJPCBStructure
	Multi Extension Card.PRJPCBVariants	A\Multi Extension Card\Multi Extension Card.PRJPCBVariants
	Multi Extension Card.SCHLIB	A\Multi Extension Card\Multi Extension Card.SCHLIB
	Skipper.SCHLIB	A Share LIB\Skipper.SCHLIB
	ZZZ-07002-ASM.OutJob	A\Multi Extension Card\ZZZ-07002-ASM.OutJob
	Skipper Bare Board.OutJob	A Share LIB\Templates\Skipper Bare Board.OutJob
	Page 1 Backplane Connector Multi Extension Card.schdoc	A\Multi Extension Card\Page 1 Backplane Connector Multi Extension Card.schdoc
	Page 2 Sensor and IO Multi Extension Card.SchDoc	A\Multi Extension Card\Page 2 Sensor and IO Multi Extension Card.SchDoc
	Page 3 CPU and LAN Multi Extension Card.SchDoc	A\Multi Extension Card\Page 3 CPU and LAN Multi Extension Card.SchDoc
	Page 4 CN2 IO and CAN Multi Extension Card.SchDoc	A\Multi Extension Card\Page 4 CN2 IO and CAN Multi Extension Card.SchDoc
	Multi Extension Card.PcbDoc	A\Multi Extension Card\Multi Extension Card.PcbDoc
	Multi Extension Card.BomDoc	A\Multi Extension Card\Multi Extension Card.BomDoc
	Multi Extension Card.PRJPCB	A\Multi Extension Card\Multi Extension Card.PRJPCB

Variants

If design files are used for several optional variants, each variant shall have its own ZIP-file followed by the revision. It is practical to keep revision in sync across all variants, even though some revision/variant combinations are out of interest.

	PCB160106001-1.zip	28.08.2017 13:42	Komprimert (zipp...	1 596 kB
	PCB160106001-2.zip	21.03.2017 11:35	Komprimert (zipp...	1 789 kB
	PCB160106001-3.zip	28.08.2017 13:42	Komprimert (zipp...	1 793 kB
	PCB160106001-4.zip	07.09.2017 09:43	Komprimert (zipp...	1 775 kB
	ZZZ-07012-E1.zip	28.08.2017 13:42	Komprimert (zipp...	25 994 kB
	ZZZ-07027-E1.zip	28.08.2017 13:42	Komprimert (zipp...	26 068 kB
	ZZZ-07027-F1.zip	21.03.2017 11:18	Komprimert (zipp...	27 592 kB
	ZZZ-07027-F2.zip	28.08.2017 13:40	Komprimert (zipp...	49 393 kB
	ZZZ-07027-F3.zip	07.09.2017 09:42	Komprimert (zipp...	38 602 kB

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Package	File Name	Path in Zip File
✓	LCD.SchDoc	A\Display Main Board\LCD.SchDoc
✓	Main Display Board 9 inch RT.PrjPcbStructure	A\Display Main Board\Main Display Board 9 inch RT.PrjPcbStructure
✓	Main Display Board 9 inch RT.PrjPcbVariants	A\Display Main Board\Main Display Board 9 inch RT.PrjPcbVariants
✓	Ethernet.SchDoc	A\Display Main Board\Ethernet.SchDoc
✓	MainBoard.PcbDoc	A\Display Main Board\MainBoard.PcbDoc
✓	Module Connector.SchDoc	A\Display Main Board\Module Connector.SchDoc
✓	Optional Com Port.SchDoc	A\Display Main Board\Optional Com Port.SchDoc
✓	RS422(NMEA).SchDoc	A\Display Main Board\RS422(NMEA).SchDoc
✓	Power supply.SchDoc	A\Display Main Board\Power supply.SchDoc
✓	Skipper.PcbLib	A Share LIB\Skipper.PcbLib
✓	Skipper.SCHLIB	A Share LIB\Skipper.SCHLIB
✓	Main Board.SchDoc	A\Display Main Board\Main Board.SchDoc
✓	ZZZ-07012-ASM.OutJob	A\Display Main Board\ZZZ-07012-ASM.OutJob
✓	ZZZ-07027-ASM.OutJob	A\Display Main Board\ZZZ-07027-ASM.OutJob
✓	MainBoard.PcbLib	A\Display Main Board\MainBoard.PcbLib
✓	Main Display Board 9 inch RT.SCHLIB	A\Display Main Board\Main Display Board 9 inch RT.SCHLIB
✓	Main Display Board 9 inch RT.PrjPcb	A\Display Main Board\Main Display Board 9 inch RT.PrjPcb

ECO (Engineering Change Order) and revisions

An ECO shall be made for all changes in schematic, PCB and/or assembly of a board. The ECO shall describe the background/reason for all changes.

Revision numbers sequences for bare boards and assembled boards shall be designed so that it is possible to understand the order of different revisions. All boards must use the same revision number series.

Skipper revision number rules for bare PCBs:

1, 2, 3..n

The revision is written as integers and stepped with any minor or major bare PCB design file change. With all bare PCB changes there must be a schematic and BOM update since the bare PCB revision is updated and the that the bare PCB is a part of the assembled PCB.

Skipper revision number rules for assembled PCBs:

A1, A1A..A1Z, A2, A2A..A2Z..A99Z, B1, B1A..B1Z, B2, B2A..B2Z..B99Z..Z99Z, AA1,AA1A..AA1Z..ZZ99Z

The first position letters (A..Z, AA..AZ..ZZ) indicates the revision level of the PCB. The second position number (1..99) indicates the revision level of the BOM/Schematic (the source of BOM is the schematic pages, all connected databases and the BomDoc). The optional third position letter (A..Z) is used if there exist a modification on the two character production revision. Modifications shall not change any production documents except the ECO itself. Added modifications means that the third letter is stepped and existing applicable ECO's are included in new ECO level. When the first letter is stepped the second number shall be set to 1. When a third letter exist and the first or second

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character is stepped, the third letter shall be dropped only if changes include all modifications. If not, the third letter is set to A.

Skipper ECO examples

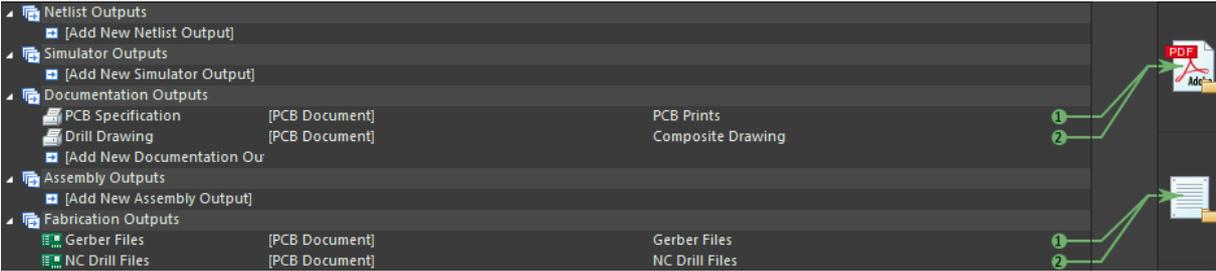
Example1: If a resistor value is changed after production for a board with revision level C2, the ECO that describes the modification will be given revision level C2A. If the BOM and schematic are updated with the new resistor value, the revision level will be C3.

Minor changes of a PCB, where there is no intention of functional changes, can be handled as a new assembly revision by stepping the second position number and changing the revision number of the PCB itself. If the changes are functional or the changes are found comprehensive, even though they are none functional, the first position letter shall be stepped.

When a modification of an existing assembled PCB is needed a modification ECO shall be issued. The modification ECO and the assembled PCB will have a third letter added to the revision number indicating the modification level. For later modification changes the third letter is stepped. All applicable modification is documented in the latest ECO. Later redesign ECO's may or may not compensate for current modification ECO's. However, redesign ECO will change the first or second revision letter/number. The third letter is removed. However, if modification is still applicable with after current redesign, both modification and redesign ECO must be issued. The third letter will in this case be reset to its starting character A.

Bare PCB production revision release

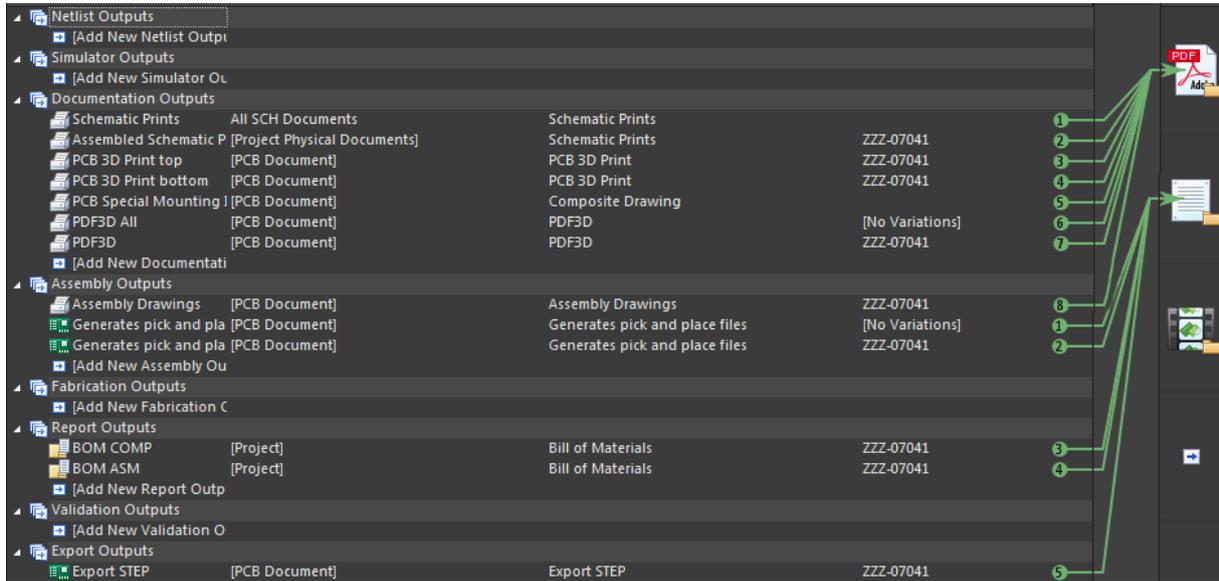
The following files and group of files must be generated with each release:



Assembled PCB production revision release

The following files and group of files must be generated with each release:

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Necessary CAD files with releases or design transfers

The following files are necessary with each release and complete design transfer between Skippers collaborators:

